This Page Is Inserted by IFW Operations
and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

As rescanning documents *will not* correct images, please do not report the images to the Image Problem Mailbox.

FIG.1

PRIOR ART

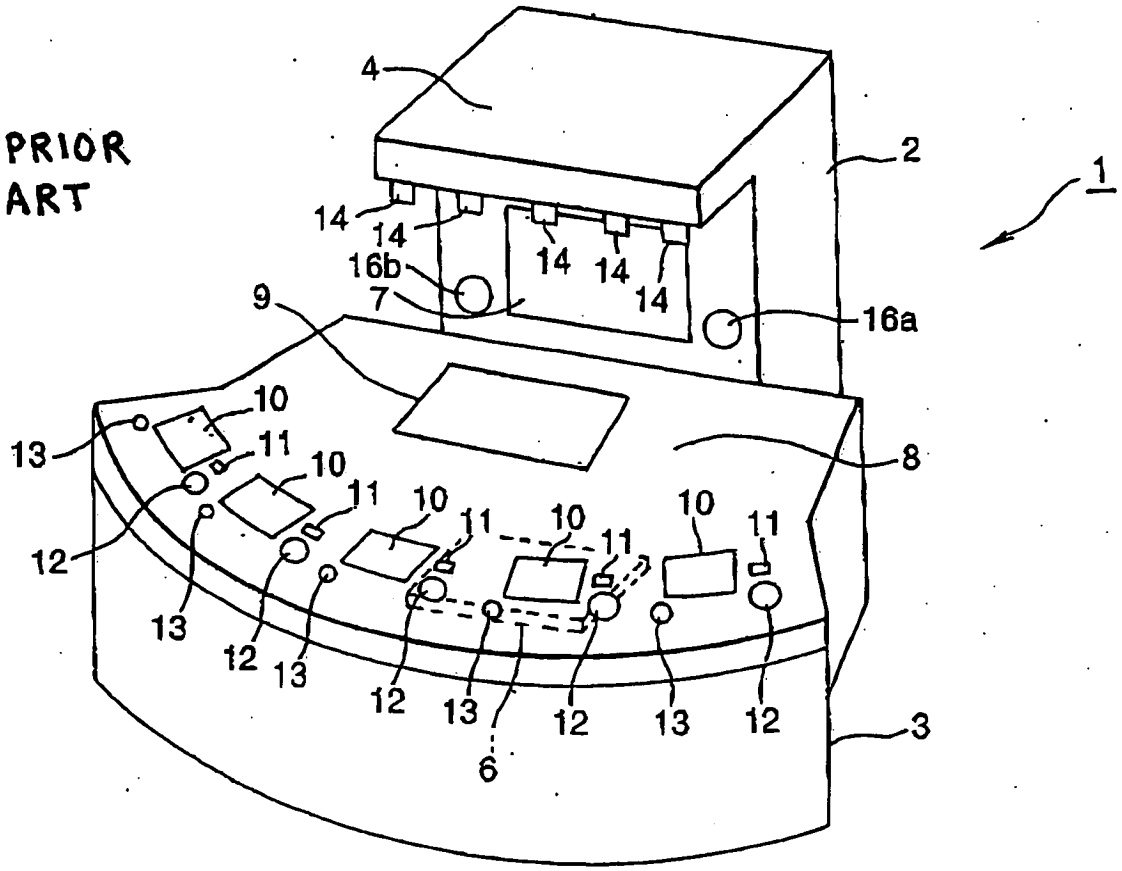


FIG.2

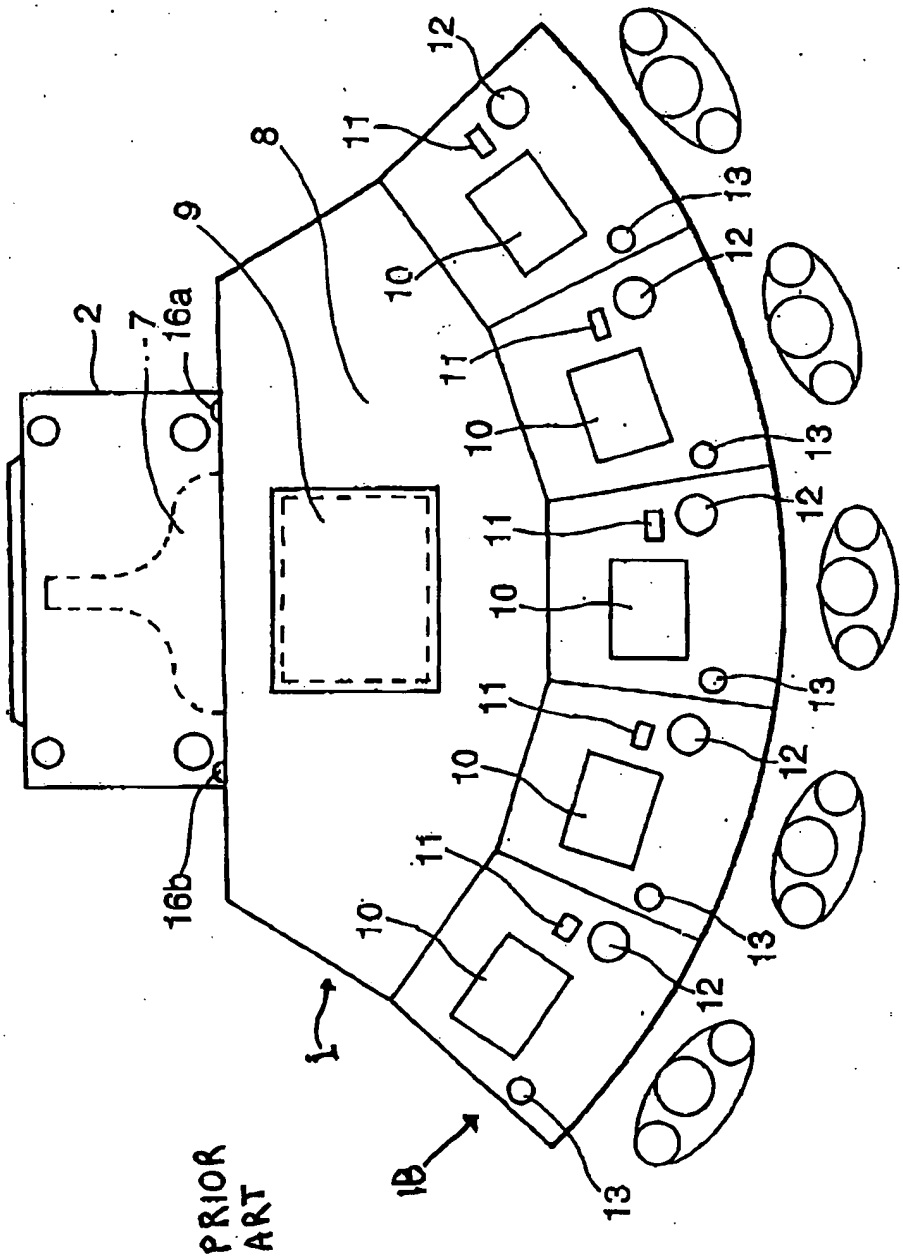


FIG.3

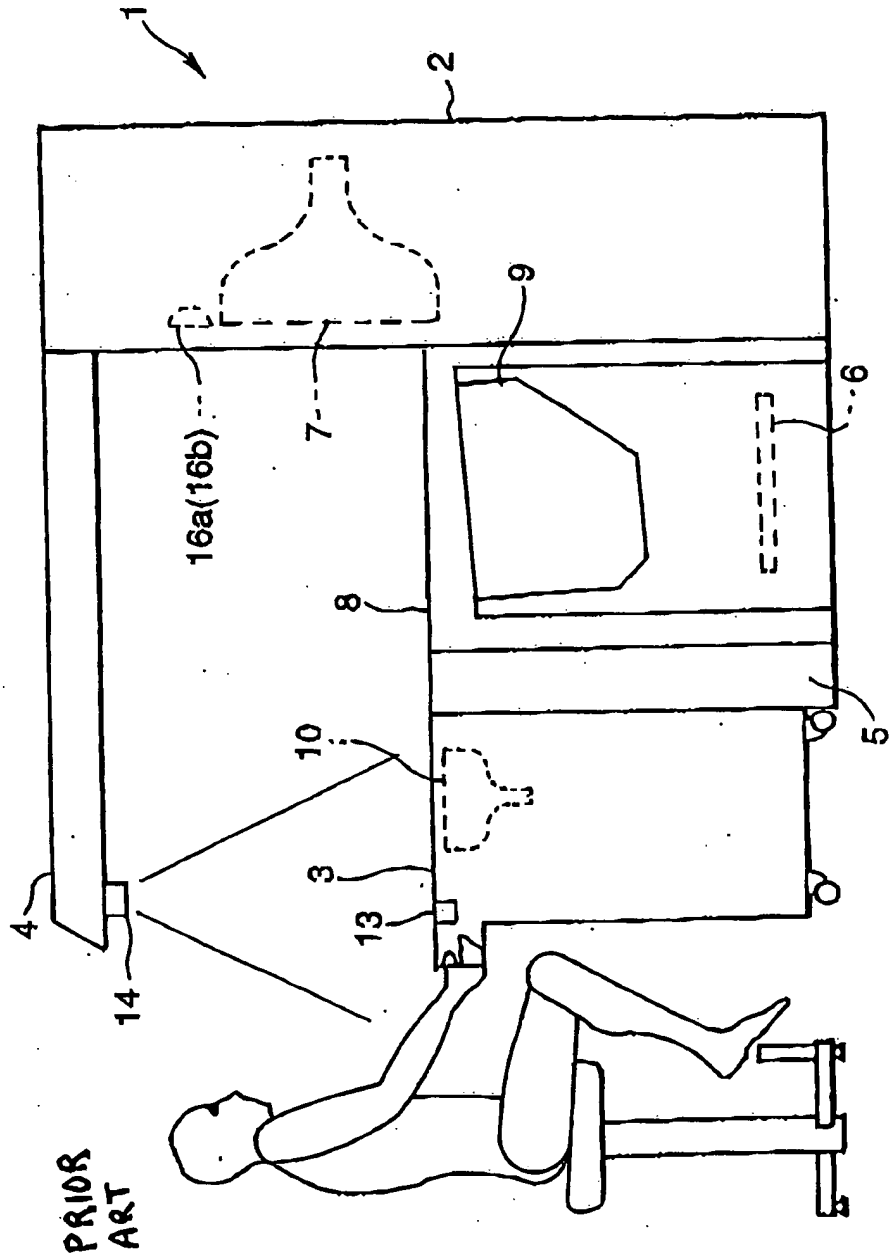


FIG.4

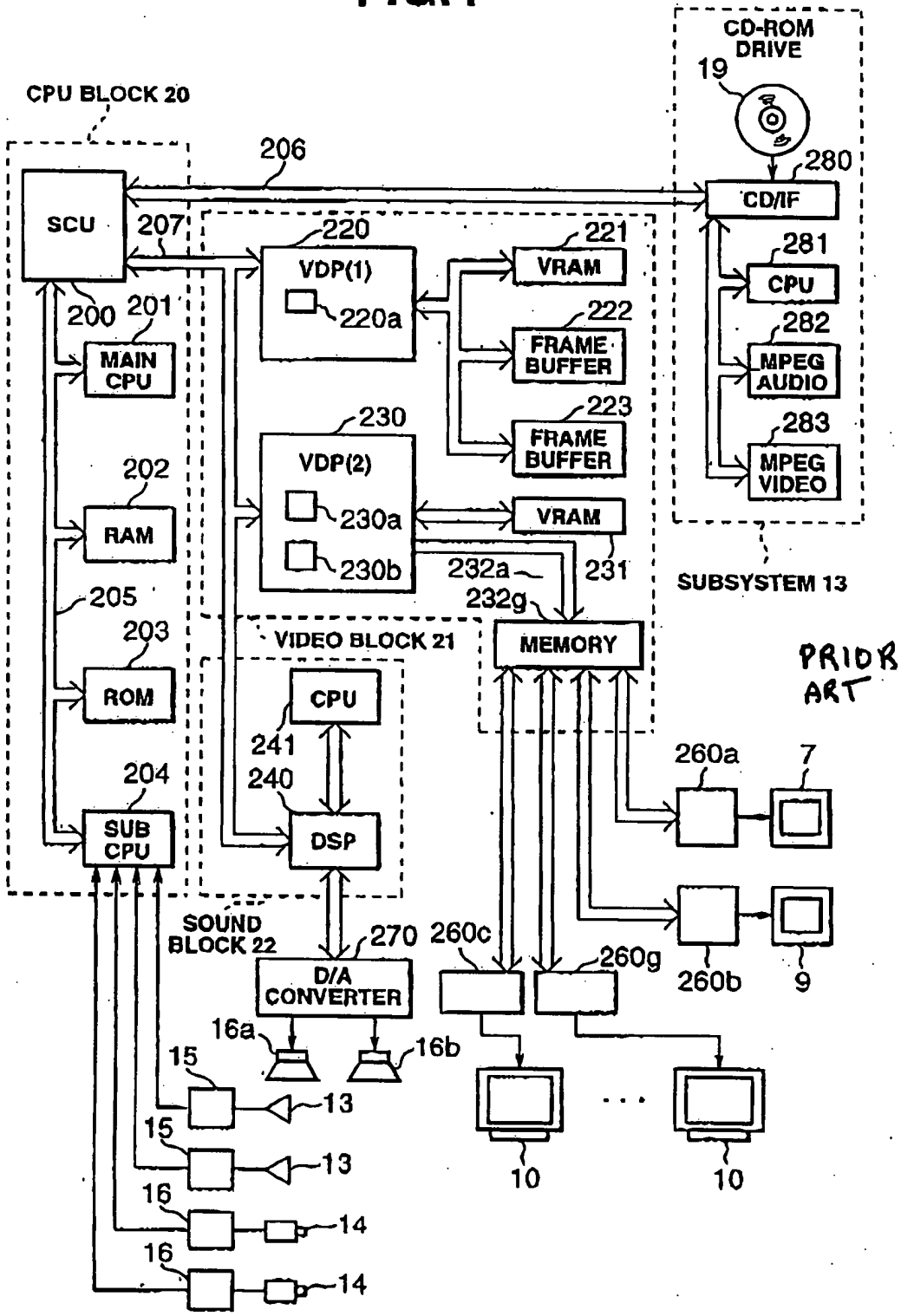

CD-ROM DRIVE
19
CPU BLOCK 20
206
280
SCU
207
CD/IF
220
221
281
VDP(1)
220a
VRAM
CPU
200
201
222
282
MAIN CPU
FRAME BUFFER
MPEG AUDIO
230
223
283
VDP(2)
FRAME BUFFER
MPEG VIDEO
202
230a
VRAM
RAM
230b
232a
231
SUBSYSTEM 13
205
232g
203
VIDEO BLOCK 21
MEMORY
PRIOR ART
ROM
CPU
241
260a
7
204
240
SUB CPU
DSP
260c
260g
SOUND BLOCK 22
270
260b
9
D/A CONVERTER
16a
16b
15
13
15
13
16
14
16
14
10
10

FIGURE 5

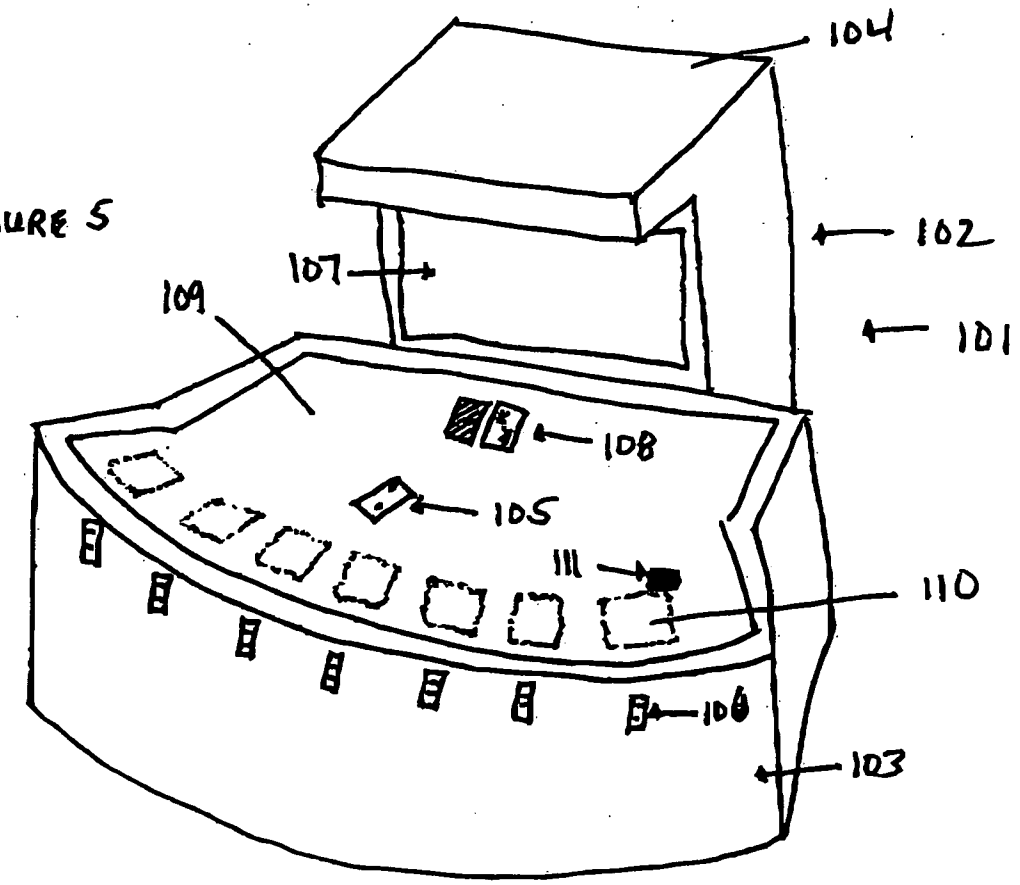


Table Master Multi-Player Platform
by
Shuffle Master

6

FIGURE

MPP Game Engine (Main Program or Dealer)

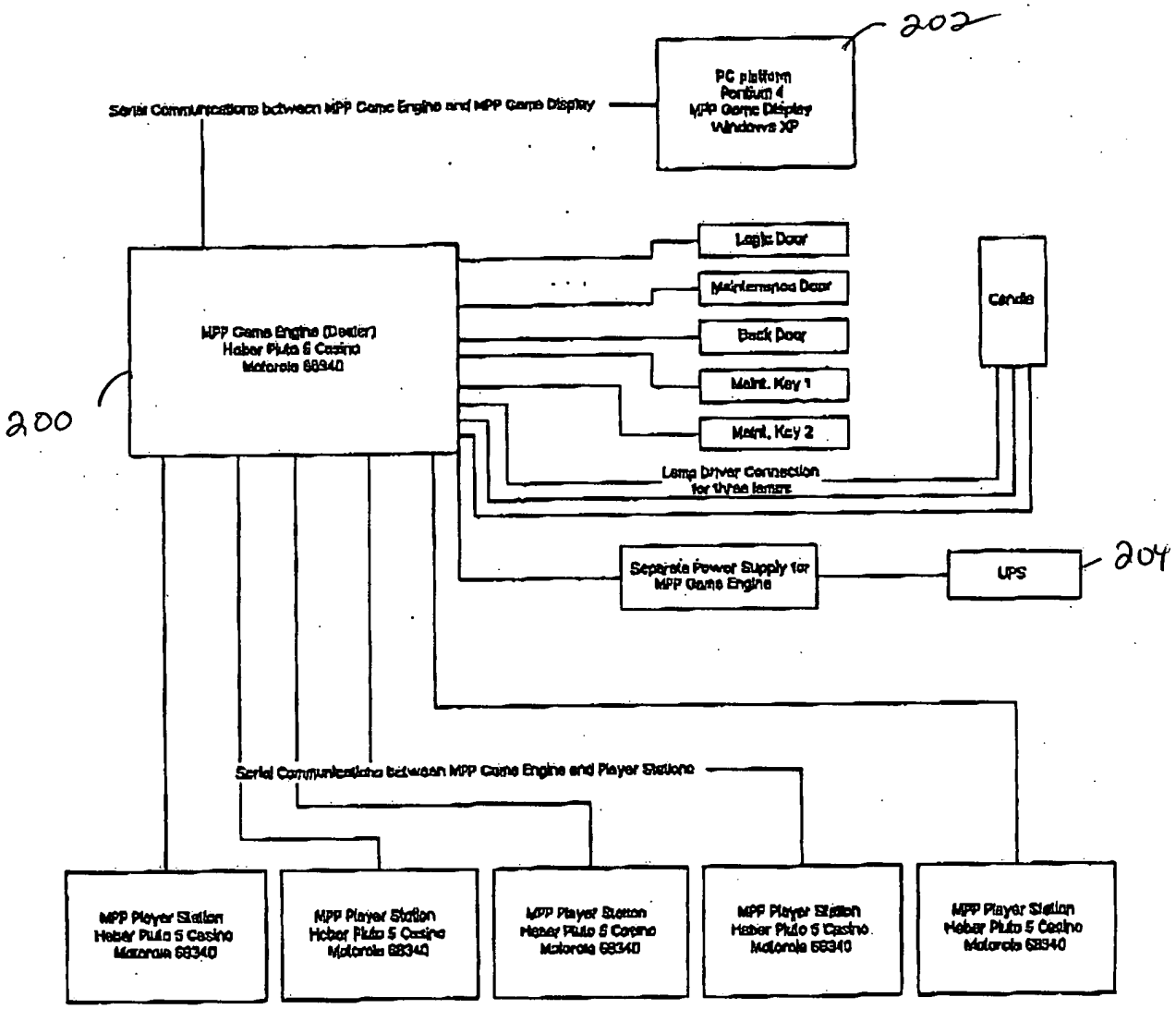


FIG. 7

MPP Player Station

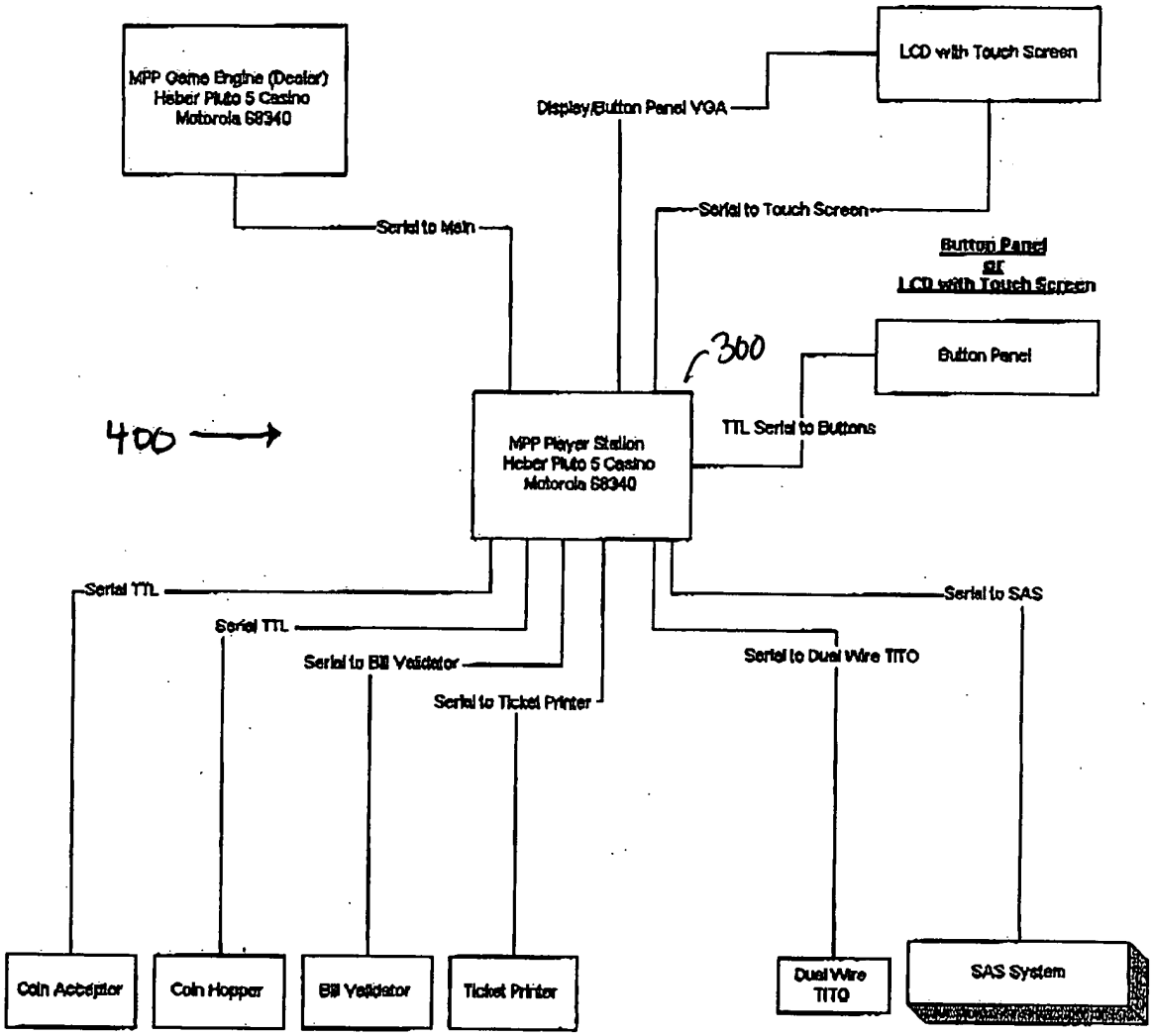
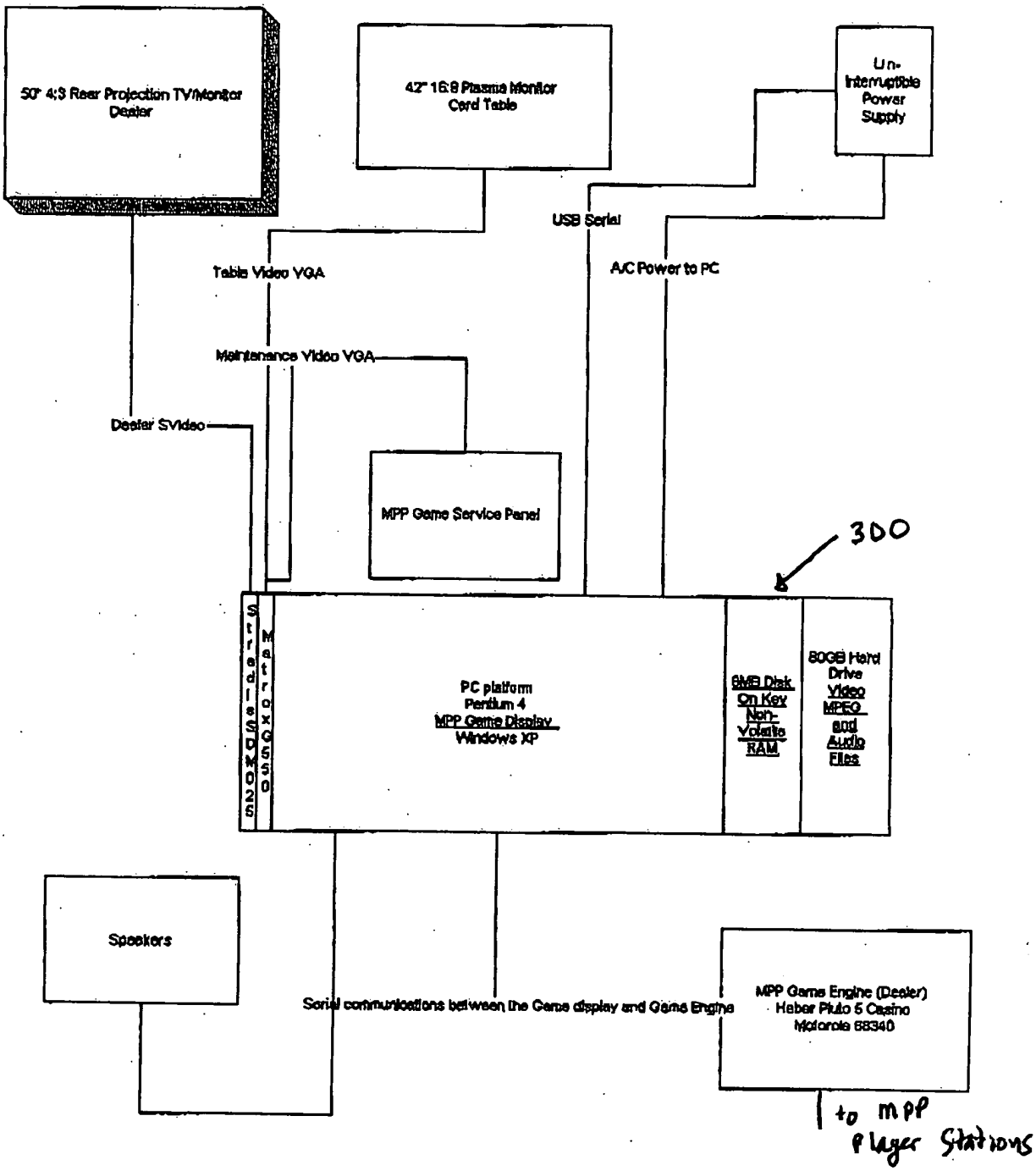


FIG. 8

MPP Game Display



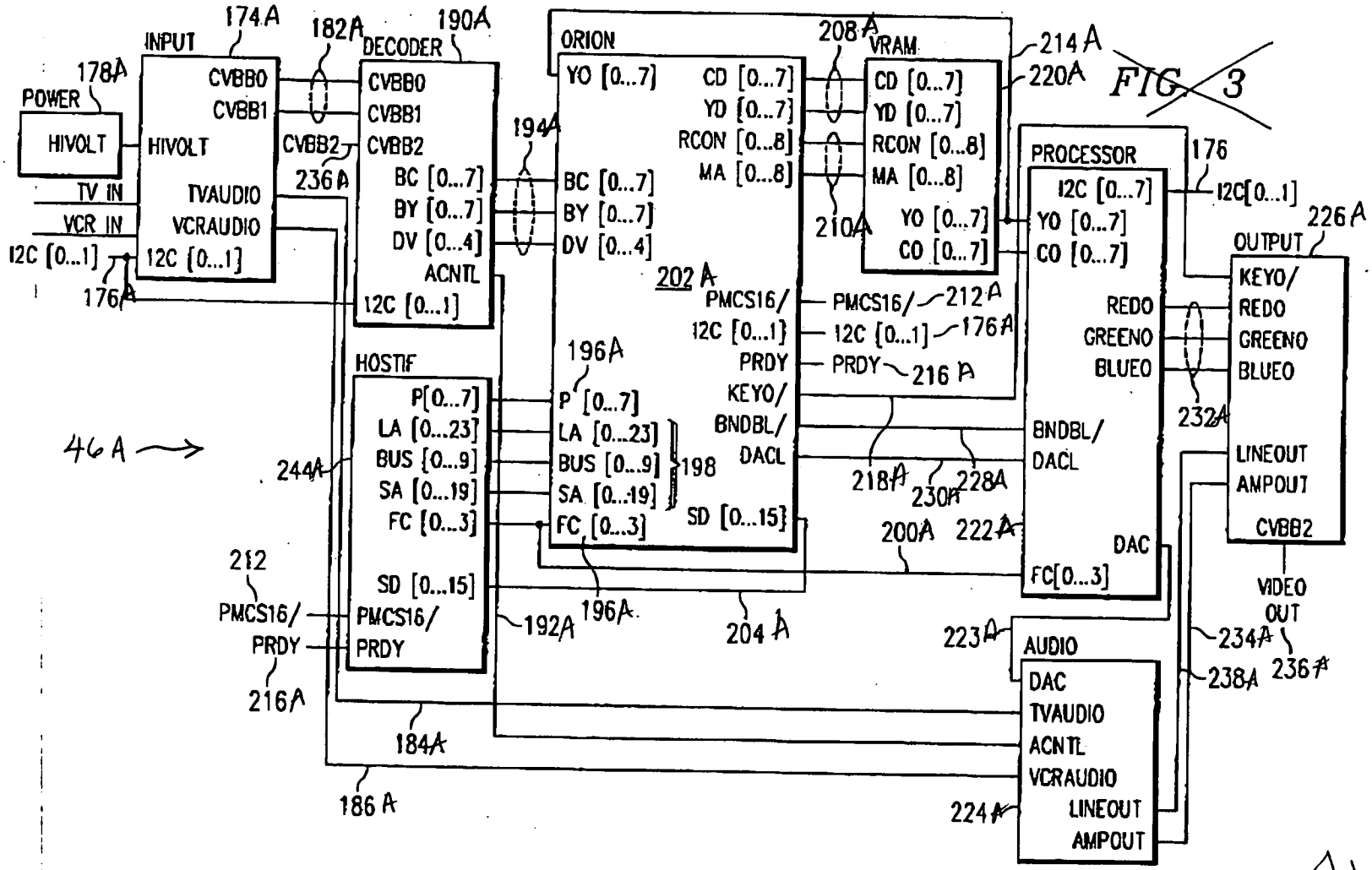

INPUT
174A
178A
POWER
HIVOLT
HIVOLT
TV IN
VCR IN
I2C [0...1]
176A
CVBB0
CVBB1
CVBB2
TVAUDIO
VCRAUDIO
I2C [0...1]
182A
DECODER
190A
CVBB0
CVBB1
CVBB2
236A
BC [0...7]
BY [0...7]
DV [0...4]
ACNTL
I2C [0...1]
194A
ORION
YO [0...7]
BC [0...7]
BY [0...7]
DV [0...4]
202A
CD [0...7]
YD [0...7]
RCON [0...8]
MA [0...8]
PMCS16/
I2C [0...1]
PRDY
KEYO/
BNDBL/
DACL
198
SD [0...15]
208A
VRAM
CD [0...7]
YD [0...7]
RCON [0...8]
MA [0...8]
210A
YO [0...7]
CO [0...7]
214A
220A
PMCS16/
212A
I2C [0...1]
176A
PRDY
216A
PROCESSOR
I2C [0...7]
YO [0...7]
CO [0...7]
REDO
GREENO
BLUEO
BNDBL/
DACL
DAC
FC[0...3]
176
I2C[0...1]
226A
OUTPUT
KEYO/
REDO
GREENO
BLUEO
232A
LINEOUT
AMPOUT
CVBB2
VIDEO OUT
234A
236A
238A
46A
HOSTIF
196A
P[0...7]
LA [0...23]
BUS [0...9]
SA [0...19]
FC [0...3]
SD [0...15]
PMCS16/
PRDY
244A
212
PMCS16/
PRDY
216A
192A
196A
204A
218A
230A
228A
200A
222A
223A
AUDIO
DAC
TVAUDIO
ACNTL
VCRAUDIO
LINEOUT
AMPOUT
224A
184A
186A

FIG 3

FIG 4

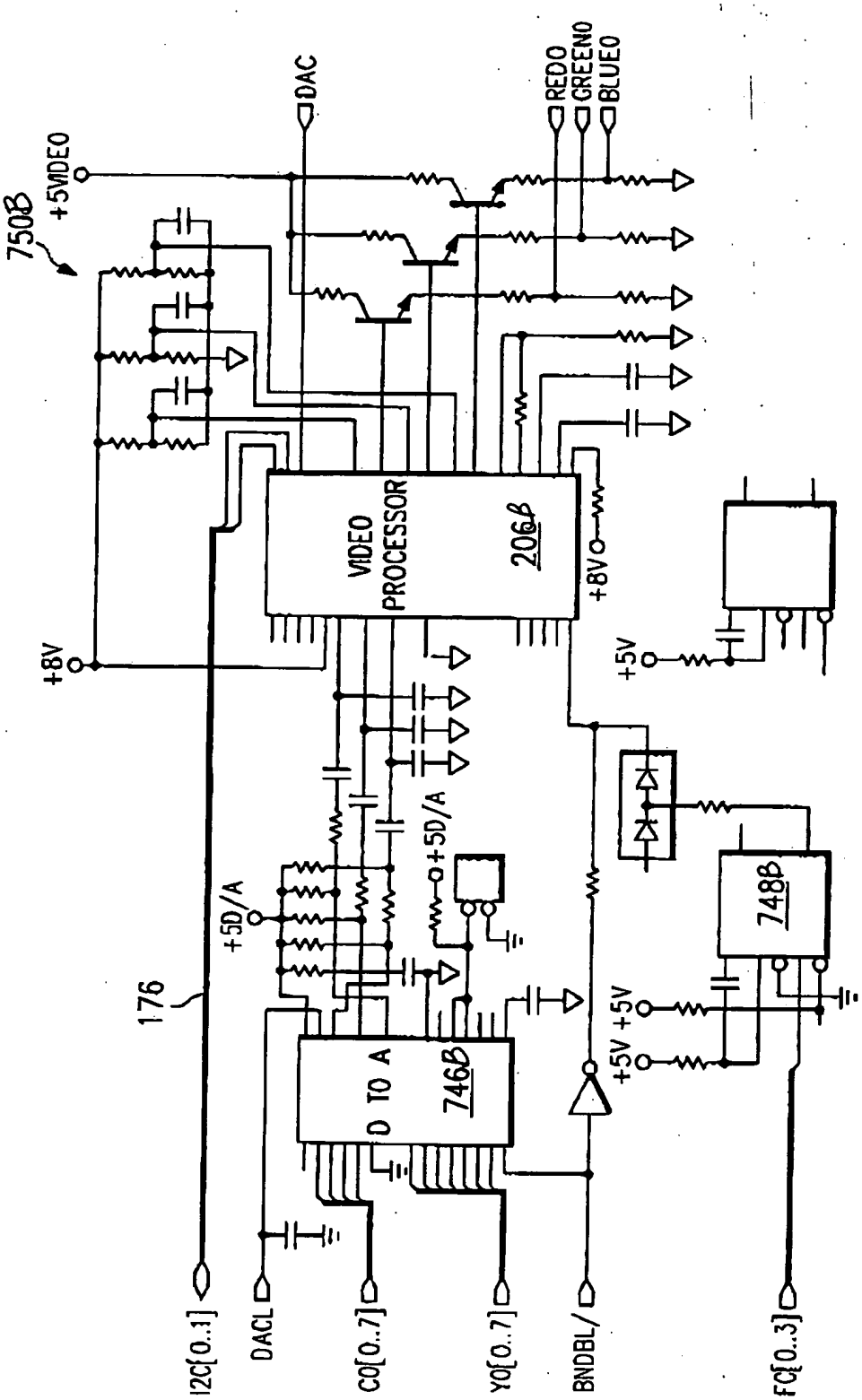

DAC
REDO
GREENO
BLUEO
+5VDEO
750B
VIDEO PROCESSOR
206B
+8V
+8V
+5V
+5D/A
+5D/A
176
D TO A
746B
748B
+5V +5V
I2C[0..1]
DACL
C0[0..7]
Y0[0..7]
BNDBL/
FC[0..3]

Fig. 10

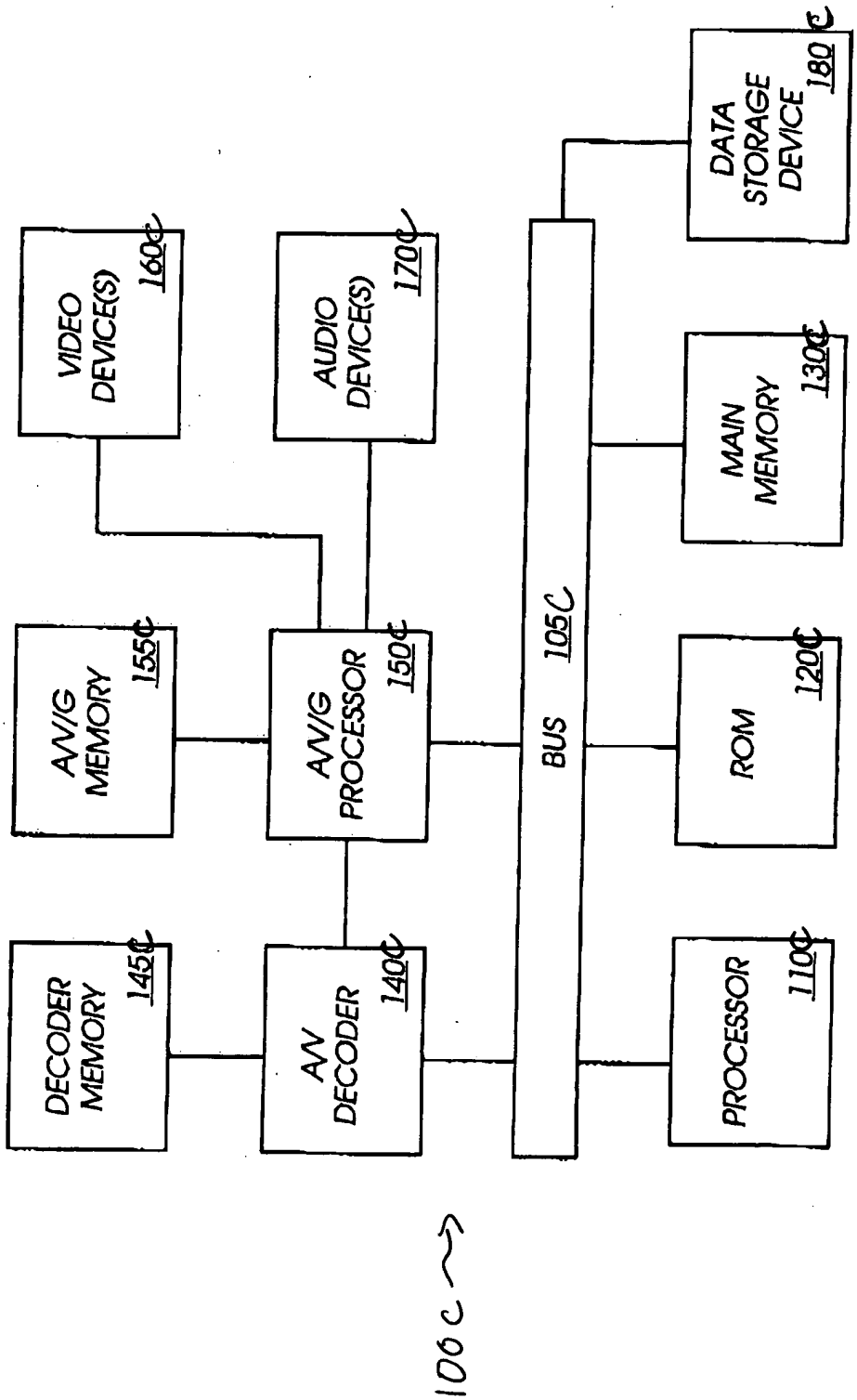
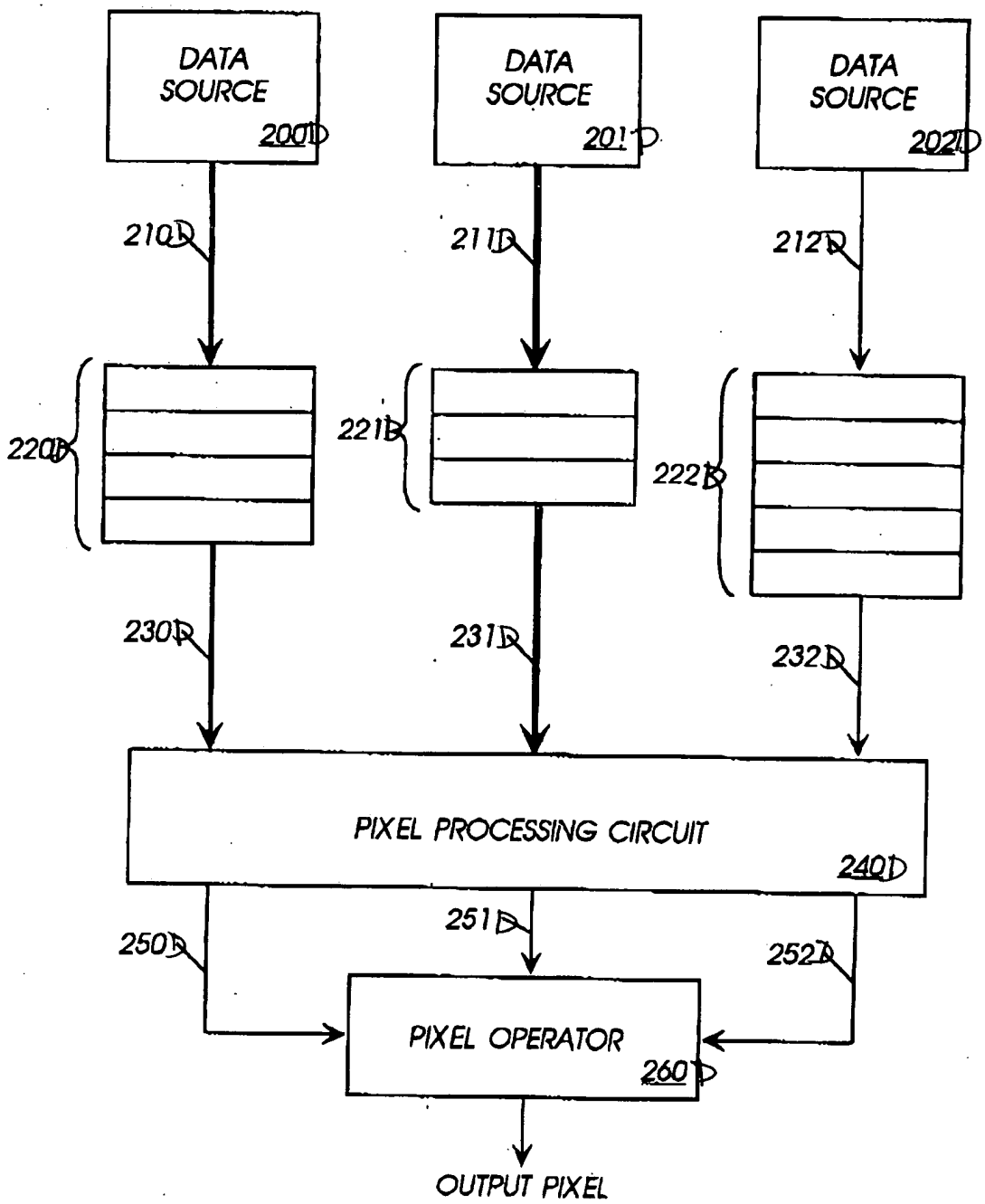

DATA STORAGE DEVICE 180
VIDEO DEVICE(S) 160
AUDIO DEVICE(S) 170
MAIN MEMORY 130
A/V/G MEMORY 155
A/V/G PROCESSOR 150
BUS 105C
ROM 120
DECODER MEMORY 145
A/V DECODER 140
PROCESSOR 110
106C

Fig. 11

Fig. 12


DATA SOURCE
200
DATA SOURCE
201
DATA SOURCE
202
210
211
212
220
221
222
230
231
232
PIXEL PROCESSING CIRCUIT
240
250
251
252
PIXEL OPERATOR
260
OUTPUT PIXEL